IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Paul Gerard D'Arcy, et al.

Serial No.:

N/A

Filed:

Herewith

For:

A REDUCTION CHECKSUM GENERATOR AND A METHOD OF

CALCULATION THEREOF

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

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7/15/03 DEBBTE SZ

Sir:

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Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Form PTO-1449. Copies of the listed references are submitted herewith.

Inventor	<u>Date</u>
Raghunath	June 25, 2002
Henriksen	November 27, 2001
Spracklen	September 28, 1999
	Henriksen

References

C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.

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Kluwer Academic Publishers, Boston; 1995; pp. 181-192.

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J. Postel; Internet Protocol; RFC 791; University of Southern California, Information Sciences Institute;

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Sciences Institute; September 1981; pp. 1-85.

J. Postel; *IUser Datagram Protocol*; RFC 768; August 1980; pp. 1-3.

L. Dadda; Some Schemes For Parallel Multipliers; Alta Frequenza; Vol. 34; pp. 349-356; 1965.

C.S. Wallace; A Suggestion For a Fast Multiplier; IEEE Transactions on Electronic Computers;

February 1964; pp. 14-17.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above

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Respectfully submitted,

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N/A (Use as many sheets as necessary) of 2 Attorney Docket Number D'ARCY 15-6-7 Sheet **U. S. PATENT DOCUMENTS** Examiner Cite **Document Number** Publication Date MM-DD-YYYY Name of Patentee or Pages, Columns, Lines, Where Applicant of Cited Document Relevant Passages or Relevant initials* No. Figures Appear Number-Kind Code^{2 (# known)} ^{US-}6,412,092 B1 06/25/2002 Raghunath

^{US-} 6,324,670 B1 11/27/2001 Henriksen ^{US-} 5,960,012 09/28/1999 Spracklen US-US-US-US-US-US-US-US-US-US-US-US-US-US-

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ 'Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	τ [¢]		

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					Application Number	N/A		
INFORMATION DISCLOSURE					Filing Date	Herewith		
STATEMENT BY APPLICANT			ICANT	First Named Inventor	Paul Gerard D'Arcy			
				Art Unit	N/A			
(Use as many sheets as necessary)				y)	Examiner Name	N/A		
$\overline{\ }$	Sheet	2	of	2	Attorney Docket Number	D'ARCY 15-6-7		

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²		
		C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.			
		K. Bickerstaff, et al.; Parallel Reduced Area Multipliers; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.			
		I. Koren; Computer Arithmetic Algorithms; Prentice-Hall, Inc.; 1993; pp. 71-72 and pp. 108-111.			
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		C.S. Wallace; A Suggestion For a Fast Multiplier; IEEE Transactions on Electronic Computers; February 1964; pp. 14-17.			

			
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